



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Confirmation No.: 5431

TANISHIMA et al.

Art Unit: 2827

Application No.: 10/633,580

Examiner: L. Pham

Filed: August 5, 2003

Docket No.: 108066-00096

For: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING  
SIMPLIFIED SHIPMENT TESTS AND REDUCED POWER CONSUMPTION

**AMENDMENT UNDER 37 C.F.R. § 1.121**

**Introductory Comments**

Director of the U.S. PTO  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

August 25, 2005

Sir:

In response to the Office Action dated May 23, 2005, the period for response being extended to September 23, 2005 by the attached Petition for Extension of Time, please amend the above-titled application as follows: